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Digital Logic Design 01

Q. 5.9: A sequential circuit has two JK flip-flops A and B and one input x. The circuit is described

Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that the AND or OR

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Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are
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This edition of Digital Design uses the latest features of IEEE Standard 1364, but only insofar as they support the authors' pedagogical objectives. The revisions and updates to the text include: Elimination of specialized circuit-level content not typically covered in a first course in logic circuits and digital design (e.g., RTL, DTL, and emitter-coupled logic circuits)

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New York and the Vanguard of Digital Design

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